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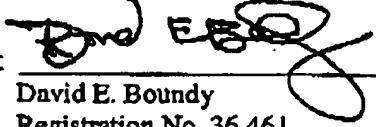
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## REQUEST TO UPDATE PALM INFORMATION

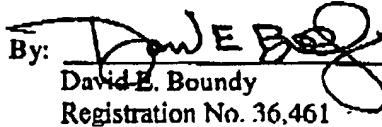
1. For the applications and patents on the first attached schedule, kindly:
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2. For the applications on the second schedule, kindly update the Attorney Docket Number as indicated on the schedule.

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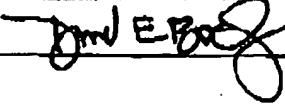
Respectfully submitted,

Dated: September 29, 2004

By:   
David E. Boundy  
Registration No. 36,461

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## UPDATE CUSTOMER NUMBER AND ATTORNEY DOCKET NO.

ATTORNEY DOCKET NO.	Patent No. Serial No.	Filing Date	Title
114596-03-4000	09/385,394	8/30/99	Computer For Executing Two Different Instruction Sets
114596-06-4003	09/322,443	5/28/99	Profiling of Computer Programs Executing in Virtual Memory Systems
114596-07-4014	09/330,852	6/11/99	Profiling Ranges of Execution of a Computer Program
114596-08-4015	09/332,263	6/11/99	Profiling Program Execution By Dense Trace Profiling and Statistical Profiling
114596-10-4017	09/334,530	6/16/99	Profiling Execution of Computer Programs
114596-12-4019	6,763,452 09/339,797	6/24/99	Modifying Program Execution Based on Profiling
114596-15-4004B	6,779,107 09/429,377	10/28/99	Improving Computer Execution by Opportunistic Adaptation
114596-16-4004C	09/429,094	10/28/99	Side Tables Annotating an Instruction Stream
114596-18-4007	6,549,959 09/434,198	11/4/99	Detecting Modification to Computer Memory by a DMA Device
114596-20-4009	09/434,394	11/4/99	Detecting Reordered Side-Effects
114596-22-4010	6,563,863 09/102,028	6/22/98	Computer Modem
114596-23-4012	6,577,639 09/323,983	6/1/99	Reducing Modem Transmit Latency

## UPDATE ATTORNEY DOCKET NO.

ATTORNEY DOCKET NO.	Serial No.	Filing Date	Title
114596-05-4013	09/239,194	1/28/99	Executing Programs for a First Computer Architecture on a Computer of a Second Architecture
114596-09-4016	09/425,401	10/22/99	Profiling Program Execution to Identify Frequently Executed Portions and to Assist Binary Translation
114596-11-4018	09/339,749	6/24/99	Profiling Program Execution into Registers of a Computer
114596-17-4006	09/428,850	10/28/99	Recording I/O Memory References in Program Execution Profile
114596-27-0052BS	09/667,226	9/21/00	Exception Mechanism for a Computer
114596-30-0126BS	09/672,841	9/28/00	Validation of Memory References
114596-31-0127BS	09/672,424	9/28/00	Complex Instruction Set Computer

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